

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	435	324/253.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/01 19:55
S2	2161	324/244,253,258,260.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 16:43
S3	36	S2 and controller and (a/d analog near3 digital a near3 d)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 16:43
S4	0	S3 and current adj amplifier	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/01 19:58
S5	24	S3 and amplifier	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/01 20:06
S6	19	LEE-WOO-JONG.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/01 20:12
S8	52	CHOI-SANG-ON.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/01 20:12
S9	12	324/244-261.ccls. and current adj amplifier and (a/d analog near3 digital a near3 d) and (fluxgate flux adj gate magnetometer magneto adj meter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 16:21
S10	58	current adj amplifier and (a/d analog near3 digital a near3 d) and (fluxgate flux adj gate magnetometer magneto adj meter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 12:45

S11	46	S10 not S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 12:19
S12	2161	324/244,253,258,260.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 12:19
S13	36	S12 and controller and (a/d analog near3 digital a near3 d)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 12:19
S14	46	S11 not S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 12:19
S15	971	amplifier and (a/d analog near3 digital a near3 d) and (fluxgate flux adj gate magnetometer magneto adj meter gradiometer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 12:45
S16	396	S15 and (cpu processor micro adj processor microprocessor controller microcontroller micro-controller) same amplifier	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 13:56
S23	435	324/253.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 15:41
S24	48	S23 and converter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 15:52
S28	45	324/244-261.ccls. and (a/d analog near3 digital a near3 d) and "and" adj gate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 16:37

S29	592	324/2\$.ccls. and (a/d analog near3 digital a near3 d) and (processor cpu microprocessor micro adj processor controller microcontroller micro adj controller clock) same (a/d analog near3 digital a near3 d) and (processor cpu microprocessor micro adj processor controller microcontroller micro adj controller clock) same control same (signal pulse output driv\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 16:46
S30	515	324/2\$.ccls. and (a/d analog near3 digital a near3 d) and (processor cpu microprocessor micro adj processor controller microcontroller micro adj controller clock) same (a/d analog near3 digital a near3 d) and (processor cpu microprocessor micro adj processor controller microcontroller micro adj controller clock) same control with (signal pulse output driv\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 16:49
S31	12	fluxgate with hall with magnetoresist\$3 and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 16:48
S35	20	324/2\$.ccls. and (a/d analog near3 digital a near3 d) and (processor cpu microprocessor micro adj processor controller microcontroller micro adj controller clock) same (a/d analog near3 digital a near3 d) and (processor cpu microprocessor micro adj processor controller microcontroller micro adj controller clock) same control with (signal pulse output driv\$3) and (processor cpu microprocessor micro adj processor controller microcontroller micro adj controller clock) same "and" adj gate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 17:16
S36	2	SONG-SEUNG-CHOUL.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 17:56
S44	24	pulse adj controller same (pulse signal output) same amplifier same (a near d analog near digital) same convert\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 18:01
S45	192	(flux adj gate fluxgate magnetometer magneto adj meter sensor) and (processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) and amplifiers and (processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) with (high low block stop prevent) with (driv\$3 saturat\$3 output signal pulse generat\$3) and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 18:13

S46	51	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) and amplifiers and (processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) with (high low block stop prevent) with (driv\$3 saturat\$3 output signal pulse generat\$3) and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 18:39
S47	5	"5223821"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 18:38
S48	7	"5333110"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 18:38
S49	24	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) and amplifiers and (processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) with (block stop prevent) with (driv\$3 saturat\$3 output signal pulse generat\$3) and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 18:40
S50	24	amplifiers and (processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) with (block stop prevent) with (driv\$3 saturat\$3 output signal pulse generat\$3) and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 18:41
S51	35	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) with (block stop prevent) with (driv\$3 saturat\$3 output signal pulse generat\$3) and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 18:58
S52	1	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) with (block stop prevent) with (driv\$3 saturat\$3 output signal pulse generat\$3) with (coil winding fluxgate flux adj gate) same (a near d analog near digital) and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:09
S53	1	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) with (block stop prevent) same (driv\$3 saturat\$3 output signal pulse generat\$3) same (coil winding fluxgate flux adj gate) same (a near d analog near digital adc) and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:10

S54	186	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu) with (block stop prevent) same (driv\$3 saturat\$3 output signal pulse generat\$3) same (coil winding fluxgate flux adj gate) same (a near d analog near digital adc)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:11
S56	164	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) same (block stop prevent) same (driv\$3 saturat\$3 output signal pulse generat\$3) same (coil winding fluxgate flux adj gate) same (a near d analog near digital adc) and "324".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:30
S57	25	samsung.as. and (fluxgate flux adj gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:17
S58	128	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) same (block stop prevent) same (driv\$3 saturat\$3 output signal pulse generat\$3) same (coil winding fluxgate flux adj gate) same (a near d analog near digital adc) same control adj signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:53
S59	12	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) same (block stop prevent) same (driv\$3 saturat\$3 output signal pulse generat\$3) same (coil winding fluxgate flux adj gate) same (a near d analog near digital adc) same control adj signal and "324".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:41
S60	4	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) same "and" adj gate same (generator) same (adc a near d analog near digital) and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:43
S62	11	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) same gate same (generat\$3) same (adc a near d analog near digital) and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:47
S63	0	pulse adj controller same (a/d adc a near d analog near digital) and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:51

S65	1	pulse adj controller and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:52
S66	3	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with wait with (a/d a near d analog near digital adc) and 324/2\$. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 19:54
S68	0	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with wait with (a/d a near d analog near digital adc) and (fluxgate flux adj gate magnetometer magneto adj meter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 20:03
S69	0	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with (on/off on near off on off block\$3 prevent\$3 stop\$4) and (processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with (a/d adc analog near3 digital a near3 digital) with (wait receive) and 324/244-253.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 07:43
S70	26	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with (on/off on near off on off block\$3 prevent\$3 stop\$4) and (processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with (a/d adc analog near3 digital a near3 digital) with (wait receive) and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 20:07
S71	28	low near power with (fluxgate flux adj gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 20:21
S72	2	("6014025").URPN.	USPAT	OR	ON	2005/04/04 20:11
S73	2	(fluxgate flux adj gate) with (signal output pulse) same (block\$3 prevent\$3 stop\$4 on/off on off on near off) same (a/d adc a near3 d analog near3 digital) and 324/253. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 20:29
S74	1	("6282803").URPN.	USPAT	OR	ON	2005/04/04 20:24

S75	12	("3888201"   "3959889"   "4262427"   "4495467"   "4622754"   "4929899"   "5010653"   "5091697"   "5161311"   "5199178"   "5255422"   "5652512").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/04 20:24
S76	1	(fluxgate flux adj gate) same (signal output pulse) with (block\$3 prevent\$3 stop\$4 on/off on off on near off) same (a/d adc a near3 d analog near3 digital) and 324/253.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 20:29
S77	8	(signal output pulse) with (block\$3 prevent\$3 stop\$4 on/off on off on near off) same (a/d adc a near3 d analog near3 digital) and 324/253.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 20:48
S78	0	(signal output pulse) with (block\$3 prevent\$3 stop\$4 on/off on off on near off) same when with (a/d adc a near3 d analog near3 digital) and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 20:48
S79	62	(signal output pulse) with (block\$3 prevent\$3 stop\$4 on/off on off on near off) same (a/d adc a near3 d analog near3 digital) and 324/244-261.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/04 20:48
S80	2687	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) and amplifiers same (coil winding fluxgate flux-gate) and core and (a/d adc a near3 d analog near3 digital) and control	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 07:45
S81	21	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) and amplifiers same (coil winding fluxgate flux-gate) and core and (a/d adc a near3 d analog near3 digital) and control	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 08:53
S83	31	S80 and 324/244,253,258,260.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/05 10:02
S84	3	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with wait with (a/d adc a near3 d analog near3 digital) and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 08:57

S85	10	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with wait same (a/d adc a near3 d analog near3 digital) and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 09:02
S86	7	S85 not S84	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 08:57
S87	10	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with (wait\$3 poll\$3) same (a/d adc a near3 d analog near3 digital) and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 09:03
S88	0	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with (wait\$3 poll\$3 sampl\$3) with when same (a/d adc a near3 d analog near3 digital) and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 09:03
S89	0	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with (wait\$3 poll\$3 sampl\$3) same when same (a/d adc a near3 d analog near3 digital) and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 09:29
S93	18	(processor microprocessor micro adj processor controller microcontroller micro adj controller cpu generator pulse near3 width near3 modulat\$3) with (wait\$3 poll\$3 until when) same (a/d adc a near3 d analog near3 digital) and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 09:31
S94	8	S93 not S87	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 09:31
S96	17	driv\$3 same "and" adj gate and (coil winding fluxgate flux-gate) and core and 324/2\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 09:54
S97	15	S96 not S95	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 10:01



S98	21	(33/271,272.ccls. 324/244,253,258,260.ccls. ) and "and" adj gate	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/05 10:02
S99	2	324/2\$.ccls. and "and" adj gate same generator same controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 11:57
S101	9	324/2\$.ccls. and "and" adj gate same (generator source) same (processor microprocessor micro adj processor controller microcontroller micro adj controller cpu)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 12:00
S102	43	324/2\$.ccls. and "and" adj gate and (generator source) same (processor microprocessor micro adj processor controller microcontroller micro adj controller cpu)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 12:04
S103	34	S102 not S101	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 12:00
S104	53	324/2\$.ccls. and "and" adj gate and (generator source clock) same (processor microprocessor micro adj processor controller microcontroller micro adj controller cpu)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 12:04
S105	10	S104 not S102	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 12:07
S106	289	324/2\$.ccls. and "and" adj gate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 12:07
S107	236	S106 not S104	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 14:45

S108	11	"5764052"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 14:45
S109	2162	324/244,253,258,260.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 16:43
S110	248	S109 and (adc a/d analog near3 digital a near3 d)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 16:43
S111	22	"and" adj gate and 324/244,253,258,260. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/04/05 17:03